UNITED STATES PATENT AND TRADEMARK OFFICE CERTIFICATE OF CORRECTION

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PATENT NO. : 6,946,637 B2 APPLICATION NO. : 10/680181

: September 20, 2005

DATED : INVENTOR(S) :

: Tetsunobu Kochi et al.

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Title page,

Item [75], Inventors, "Tetsunobu Kochi, Kanagawa-ken (JP); Shigetoshi Sugawa, Kanagawa-ken (JP): Isamu Ueno, Kanagawa-ken (JP); Katsuhisa Ogawa, Tokyo (JP); Toru Koizumi, Kanagawa-ken (JP); Katsuhito Sakurai, Tokyo (JP); Hiroki Hiyama, Kanagawa-ken (JP)" should read -- Tetsunobu Kochi, Hiratsuka (JP); Shigetoshi Sugawa, Atsugi (JP); Isamu Ueno, Hadano (JP); Katsuhisa Ogawa, Machida (JP); Toru Koizumi, Yokohama (JP); Katsuhito Sakurai, Machida (JP); Hiroki Hiyama, Atsugi (JP) --.

Column 1,

Line 23, "provide" should read -- provides --.

Column 2,

Line 40, "is" should read -- are --.

Column 8,

Line 7, "Vth1," should read -- Vth1, --.

Column 11.

Line 55, "denotes are" should read -- denote --.

Column 12,

Line 8, "are" should read -- is --; and

Line 36, "phenomena" should read -- phenomenon --.

Column 15,

Line 18, "manner that" should read -- manner as that --; and

Line 23, "that" should read -- as that --.

Column 17,

Line 56, "are" should read -- is --; and

Line 57, "photo-charge" should read -- photo-charges --.

Column 18,

Line 55, "regist" should read -- resist --.

Column 19,

Line 57, "first and" should read -- first switch and --; and

Line 64, "thickness" should read -- thicknesses --.

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It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Column 20,

Line 26, "charge" should read -- signal charge --.

Column 22,

Line 4, "transistor," should read -- transistors, --.

Signed and Sealed this

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Twenty-seventh Day of June, 2006

JON W. DUDAS Director of the United States Patent and Trademark Office